Microprocessor basic concepts, processor architectures, memory addressing and addressing modes, programming with assembly language, stack and segments, data types, arithmetic and logic instruction sets, branch and loop usage, interrupts, BIOS programming, macros, signed numbers and arrays. The Intel Microprocessors (8th Edition) by Barry B. Brey, 2008. Microprocessors and Microcontrollers (Second Edition) by R.S. Kaler, 2013. Microprocessors and Microcontrollers (Second Edition) by R.S. Kaler, 2013. Microprocessors and Microcontrollers (Second Edition) by R.S. Kaler, 2013. Microprocessors and Microcontrollers (Second Edition) by R.S. Kaler, 2013. There is no prerequisite or co-requisite for this course. Course Type	COURSE DESCRIPTION FORM							
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Catalogue Data of the Course (Course Content) Course Textbooks Microprocessor basic concepts, processor architectures, memory addressing and addressing modes, programming with assembly language, stack and segments, data segment and data types, arithmetic and logic instruction sets, branch and loop usage, interrupts, BIOS programming, marcos, signed numbers and arrays. Course Textbooks Antonakos, James L., An introduction to the Intel family of microprocessors, Prentice Hall, 1999. Antonakos, James L., An introduction to the Intel family of microprocessors, Prentice Hall, 1999. There is no perequisite or co-requisite for this course. Course (Intendance Requirements) Course Type Compulsory Language of Instruction It is aimed to be learned the structure of microprocessors, memory addressing, addressing modes, segments, stacks, command sets and implementing all concepts with assembly language during laboratory hours. 1. Having knowledge about microprocessors 2. Learning concepts of stack, subroutine, macro, interrupts 4. Learning the assembly language to develop low-level programs. 5. Writing basic programs with assembly language Instruction Method (Face-to-face, Distance education etc.) In Week: Introduction to microprocessors, basic concepts 2. Week: Wrogramming with assembly language 3. Week: Memory addressing, addressing modes, 4. Week: Programming with assembly language 5. Week: Togramming with assembly language 6. Week: Stacks and segments Weekly Schedule of the Course Weekly Schedule of the Course Weekly Schedule of the Course Weekl Programming with assembly language 5. Week: Arithmetic and logic instruction sets 9. Week: Arithmetic and logic instruction sets 9. Week: Arithmetic and logic instruction sets 10. Week: Branch, call and long usage 11. Week: Interrupts, INT2IH and INTOH instructions 12. Week Bros Programming with assembly language 13. Week: Marcors 14. Week: Stacks and segments 15. Weekl: Programming with assembly language 16. Week: Stacks and segments 17. Week	Course Code and Name							
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Antonakos, James L., An introduction to the Intel family of microprocessors, Prentice Hall, 1999. Microprocessors and Microcontrollers (Second Edition) by R.S. Kaler, 2013. Credit (ECTS) 6 There is no prerequisite or co-requisite for this course. Course (Attendance Requirements) Course (Intendance Requirements) It is aimed to be learned the structure of microprocessors, memory addressing, addressing modes, segments, stacks, command sets and implementing all concepts with assembly language develop low-level programs. 1. Having knowledge about microprocessors 2. Learning microprocessors architectures, instruction sets, addressing modes. 3. Learning microprocessor architectures, instruction sets, addressing modes. 4. Learning the assembly language to develop low-level programs. 5. Writing basic programs with assembly language The mode of delivery of this course is face to face. 1. Week: Introduction to microprocessors, basic concepts 2. Week: Microprocessor architectures 3. Week: Memory addressing, addressing modes, 4. Week: Programming with assembly language 5. Week: Programming with assembly language 6. Week: Stacks and segments 7. Week: Dara segment and data types 8. Week: Arithmetic and logic instruction sets 9. Week: Arithmetic and logic instruction sets 9. Week: Arithmetic and logic instruction sets 9. Week: Arithmetic and logic instruction sets 10. Week: Branch, call and lopo usage 11. Week: Interrupts, INT21H and INT10H instructions 12. Week: Biol Sop programming 13. Week: Microprocessor architectures Teaching Activities (The time spent for the activities listed here will determine the amount of credit required)	Catalogue Data of the Course (Course Content)	addressing modes, programming with assembly language, stack and segments, data segment and data types, arithmetic and logic instruction sets, branch and loop usage, interrupts, BIOS programming, macros, signed numbers and arrays.						
Hall, 1999. Microprocessors and Microcontrollers (Second Edition) by R.S. Kaler, 2013.	Course Textbooks	The Intel Microprocessors (8th Edition) by Barry B. Brey, 2008.						
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Course Type Compulsory	Prerequisites for the Course (Attendance Requirements)	There is no prerequisite or co-requisite for this course.						
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Weekly practical course hours:2 Reading Activities (The time spent for the activities listed here will determine the amount of credit required) Assessment Criteria Weekly practical course hours:2 Reading activities Internet search and library work Designing and implementing materials Making a report Preparing and making presentations Midterm and revision for midterm Final exam and revision for final exam Number(s) Weight (%) Midterm exam 1 25	Weekly Schedule of the Course	 Week: Microprocessor architectures Week: Memory addressing, addressing modes, Week: Programming with assembly language Week: Programming with assembly language Week: Stacks and segments Week: Data segment and data types Week: Arithmetic and logic instruction sets Week: Arithmetic and logic instruction sets Week: Branch, call and loop usage Week: Interrupts, INT21H and INT10H instructions Week: BIOS programming Week: Macros 						
Assessment Criteria Midterm exam 1 25	Teaching Activities (The time spent for the activities listed here will determine the amount of credit required)	Weekly practical course hours:2 Reading activities Internet search and library work Designing and implementing materials Making a report Preparing and making presentations Midterm and revision for midterm						
Midterm exam 1 25			Number(s)	Weight (%)				
	Assessment Criteria	Midterm exam Assignment		25				

	Application 10			15							
	Project	:	1	20							
	Practice		0	20							
	Quiz 0										
	Final exam 1				40						
	Total				100						
	Activity		Number of Weeks	Duration (Weekly Hour)		ly S	End of Semester Total Workload				
	Weekly the	oretical course	hours	14				42			
		ctical course he		10		2		20			
	Reading activities		14		2		28				
	Internet search and library work		12	1			12				
Workload of the Course	Designing and implementing materials		1	12			12				
Working of the Course	Making a report		1	4			4				
	Preparing a	nd making pres	sentations	1		2		2			
		d revision for r		1		15			15		
	Final exam and revision for final			1		15		15			
	exam			-	1						
	Total workload							150			
	Total workload/ 25			+			6				
	Course Credit (ECTS)						6				
Contribution Level	No					1	2	3	4	5	
between Course Outcomes				cs, science, basic	;						
and Program Outcomes	1	engineering, o								X	
	1			e this knowledge	in					$ \Lambda $	
				ring problems.							
				nte and analyze blems using basi							
		science, math									
	2	knowledge an								X	
		Sustainable Development Goals relevant to									
		the problems									
	3	complex engi	design creative solutions to engineering problems; ability to								
		current and future requirements, considering realistic constraints and conditions. Ability to select, use and develop appropriate						X			
		techniques, resources and modern engineering and informatics tools, including			nœ						
	4	estimation and modeling, for the analysis and solution of complex engineering problems							X		
		while being a									
				ethods to examin							
		complex engineering problems or research topics in computer engineering, including									
	5	reviewing the							X		
		experiments,							Λ		
				g and interpreting							
		results.									
	6	Knowledge of the effects of engineering									
		practices and the standards used in these practices on society, health and safety,									
		economy, sustainability and environment									
				N Sustainable							
		Development									
				ring solutions in	the						

		fields of information security and law.		
	7	Acting in accordance with engineering professional principles and knowledge on ethical responsibility; awareness of acting impartially, without discrimination on any issue, and being inclusive of diversity.	X	
	8	Ability to work effectively individually and as a team member or leader in intradisciplinary and multidisciplinary teams (face-to-face, remote, or hybrid).		X
	9	Ability to conduct effective verbal and written communication on technical issues in Turkish or English, prepare reports, make effective presentations and prepare software documentation, considering the various differences of the target audience (such as education, language, profession).	X	
	10	Knowledge of business practices such as project, risk and change management and economic feasibility analysis; awareness of entrepreneurship and innovation.		X
	11	Lifelong learning skill that includes the ability to learn independently and continuously, to adapt to new and developing scientific practices and technologies, and to think inquisitively about technological changes.	X	
Lecturer(s) and Contact Information		er Dr. Muhammet Ünal ıhunal@gazi.edu.tr		