<table>
<tr><th colspan="2">COURSE DESCRIPTION FORM</th></tr>
<tr><td>Course Code and Name</td><td>BM486 VLSI DESIGN (TECH. ELECT.)</td></tr>
<tr><td>Course Semester</td><td>8</td></tr>
<tr><td>Catalogue Data of the Course (Course Content)</td><td>Information on modern microelectronic circuit design and applications.</td></tr>
<tr><td>Course Textbooks</td><td>CMOS VLSI Design: A Circuits and Systems Perspective (4th Edition) by Neil Weste, David Harris, 2010.</td></tr>
<tr><td>Supplementary Textbooks</td><td>VLSI Digital Signal Processing Systems: Design and Implementation by Keshab K. Parhi, 1999.
VLSI Design (VLSI Circuits) 1st Edition by M. Michael Vai, 2000.</td></tr>
<tr><td>Credit (ECTS)</td><td>6</td></tr>
<tr><td>Prerequisites for the Course (Attendance Requirements)</td><td>-</td></tr>
<tr><td>Course Type</td><td>Elective</td></tr>
<tr><td>Language of Instruction</td><td>English</td></tr>
<tr><td>Course Objectives</td><td>To provide knowledge and skills in the field of modern integrated circuit design, ensuring the effective utilization of integrated circuit tools and software.</td></tr>
<tr><td>Course Learning Outcomes</td><td>1.Grasps the integrated design flow.
2.Effectively utilizes computer-aided design environments.
3.Learns integrated component models.
4.Designs integrated basic analog structure blocks using software in accordance with desired performance parameters.</td></tr>
<tr><td>Instruction Method
(Face-to-face, Distance education etc.)</td><td>Face-to-face</td></tr>
<tr><td>Weekly Schedule of the Course</td><td>Week 1. Very Large Scale Integration (VLSI) design methods
Week 2. Very Large Scale Integration (VLSI) design methods
Week 3. Design verification and test methods
Week 4. Adders, multipliers, counters
Week 5. Arithmetic Logic Unit (ALU)
Week 6. Memories and Finite State Machine (FSM) structures
Week 7. Synchronization, meta-stability
Week 8. PLL and DLL circuits
Week 9. PLL and DLL circuits
Week 10. Integrated circuit designs with Programmable Logic Devices (CPLD, FPGA, FPLD)
Week 11. Integrated circuit designs with Programmable Logic Devices (CPLD, FPGA, FPLD)
Week 12. Introduction to Hardware Description Language (HDL)
Week 13. Integrated circuit design using HDL with computer-aided design tools
Week 14. Integrated circuit design using HDL with computer-aided design tools.</td></tr>
<tr><td>Teaching Activities
(The time spent for the activities listed here will determine the amount of credit required)</td><td>Weekly theoretical course hours:3
Reading activities
Internet search and library work
Designing and implementing materials
Making a report
Preparing and making presentations
Midterm and revision for midterm
Final exam and revision for final exam</td></tr>
<tr><td>Assessment Criteria</td><td>
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<tr><th></th><th>Number(s)</th><th>Weight (%)</th></tr>
<tr><td>Midterm exam</td><td>1</td><td>30</td></tr>
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	Assignment		
	Application		
	Project	1	30
	Practice		
	Quiz		
	Final exam	1	40
	Total	3	100

Workload of the Course

Activity	Number of Weeks	Duration (Weekly Hour)	End of Semester Total Workload
Weekly theoretical course hours	14	3	42
Weekly practical course hours			
Reading activities	14	2	28
Internet search and library work	14	2	28
Designing and implementing materials	1	10	10
Making a report	1	10	10
Preparing and making presentations	1	10	10
Midterm and revision for midterm	1	15	15
Final exam and revision for final exam	1	15	15
Total workload			150
Total workload/ 25			6
Course Credit (ECTS)			6

Contribution Level between Course Outcomes and Program Outcomes

No	Program Outcomes	1	2	3	4	5
1	Knowledge of mathematics, science, basic engineering, computing, and computer engineering; ability to use this knowledge in solving complex engineering problems.				X	
2	Ability to define, formulate and analyze complex engineering problems using basic science, mathematics and engineering knowledge and considering the UN Sustainable Development Goals relevant to the problems addressed.				X	
3	Ability to design creative solutions to complex engineering problems; ability to design complex systems, processes, devices, software, algorithms or products to meet current and future requirements, considering realistic constraints and conditions.				X	
4	Ability to select, use and develop appropriate techniques, resources and modern engineering and informatics tools, including estimation and modeling, for the analysis and solution of complex engineering problems while being aware of their limitations.				X	
5	Ability to use research methods to examine complex engineering problems or research topics in computer engineering, including reviewing the literature, designing experiments, conducting experiments, collecting data, analyzing and interpreting results.			X		
6	Knowledge of the effects of engineering practices and the standards used in these practices on society, health and safety, economy, sustainability and environment within the scope of the UN Sustainable Development Goals; awareness of the		X			

	consequences of engineering solutions in the fields of information security and law.						
7	Acting in accordance with engineering professional principles and knowledge on ethical responsibility; awareness of acting impartially, without discrimination on any issue, and being inclusive of diversity.		X				
8	Ability to work effectively individually and as a team member or leader in intradisciplinary and multidisciplinary teams (face-to-face, remote, or hybrid).			X			
9	Ability to conduct effective verbal and written communication on technical issues in Turkish or English, prepare reports, make effective presentations and prepare software documentation, considering the various differences of the target audience (such as education, language, profession).			X			
10	Knowledge of business practices such as project, risk and change management and economic feasibility analysis; awareness of entrepreneurship and innovation.		X				
11	Lifelong learning skill that includes the ability to learn independently and continuously, to adapt to new and developing scientific practices and technologies, and to think inquisitively about technological changes.		X				

Lecturer(s) and Contact Information	Lecturer's First/Last Name: Asst. Prof. Dr. Feyza YILDIRIM OKAY E-mail address: feyzaokay@gazi.edu.tr